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APPLICANTS

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** CONTINUING DATA *****
 This application is a DIV of 10/057,205 01/25/2002 PAT 6,851,183 *verified. EP*

** FOREIGN APPLICATIONS *****
 UNITED KINGDOM 0126821.8 11/07/2001 *verified. EP*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>[Signature]</i> <i>EP</i> Examiner's Signature Initials	STATE OR COUNTRY UNITED KINGDOM	SHEETS DRAWING 3	TOTAL CLAIMS 78	INDEPENDENT CLAIMS 5
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TITLE
 Semiconductor package assembly and method for electrically isolating modules

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of
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